

FIG. 1 is a block diagram of a system architecture.

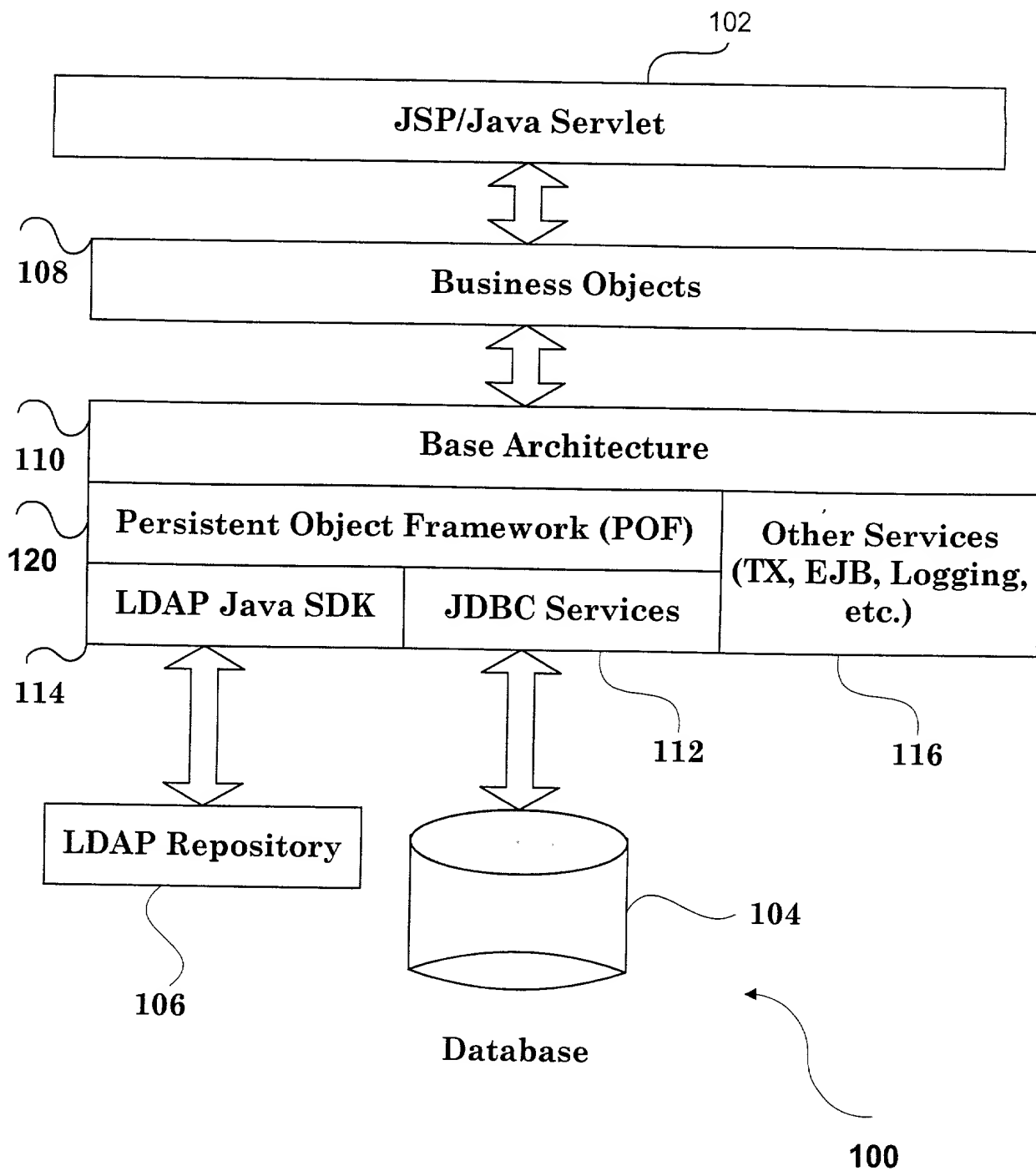


Fig. 1

120

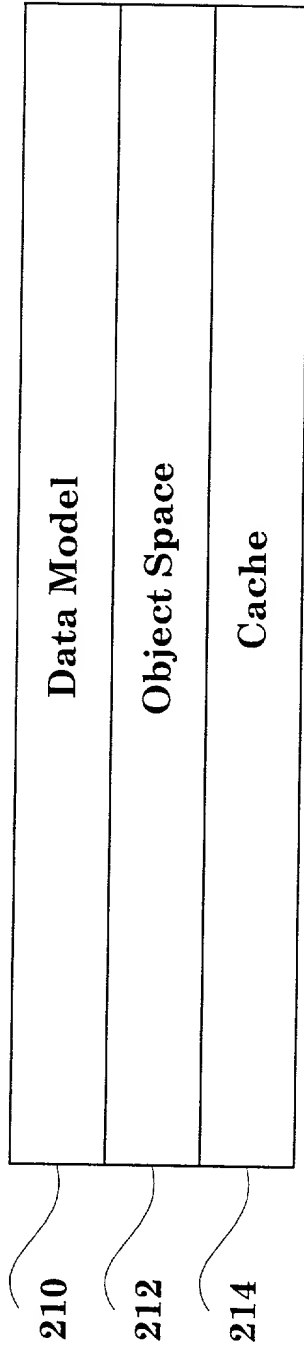


Fig. 2

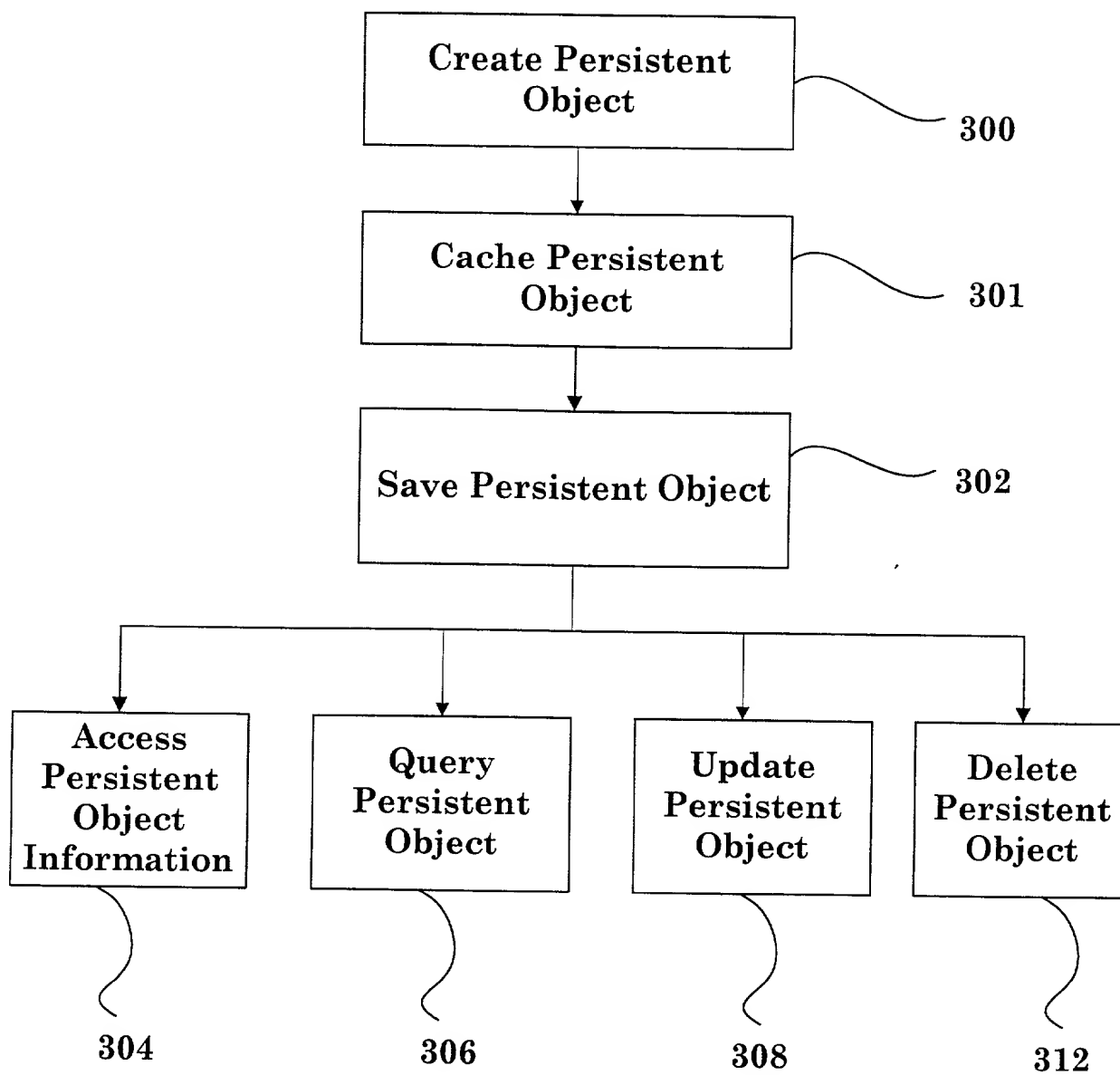


Fig. 3

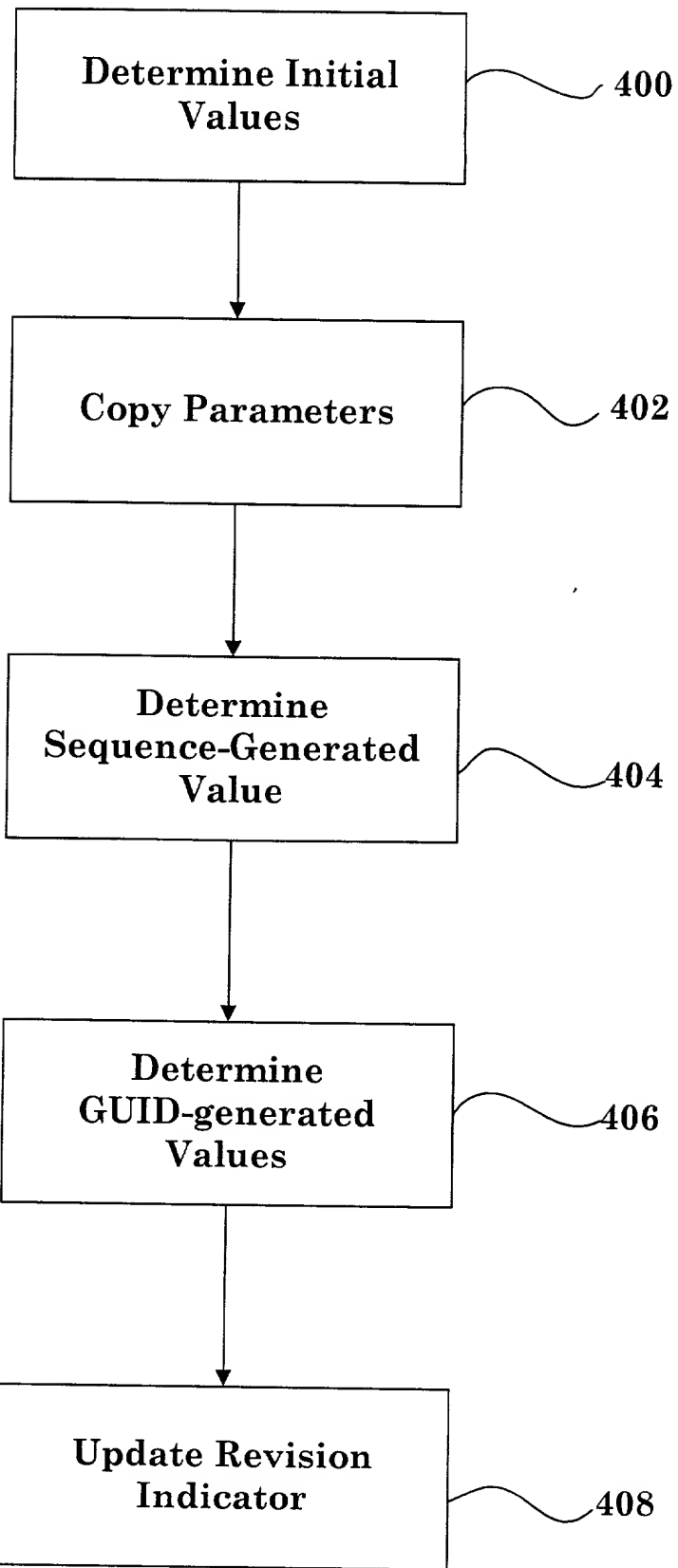


Fig. 4A

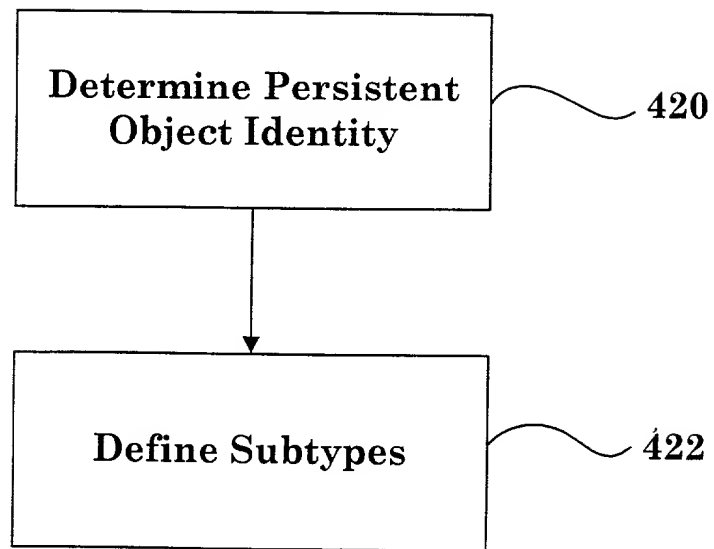


Fig. 4B

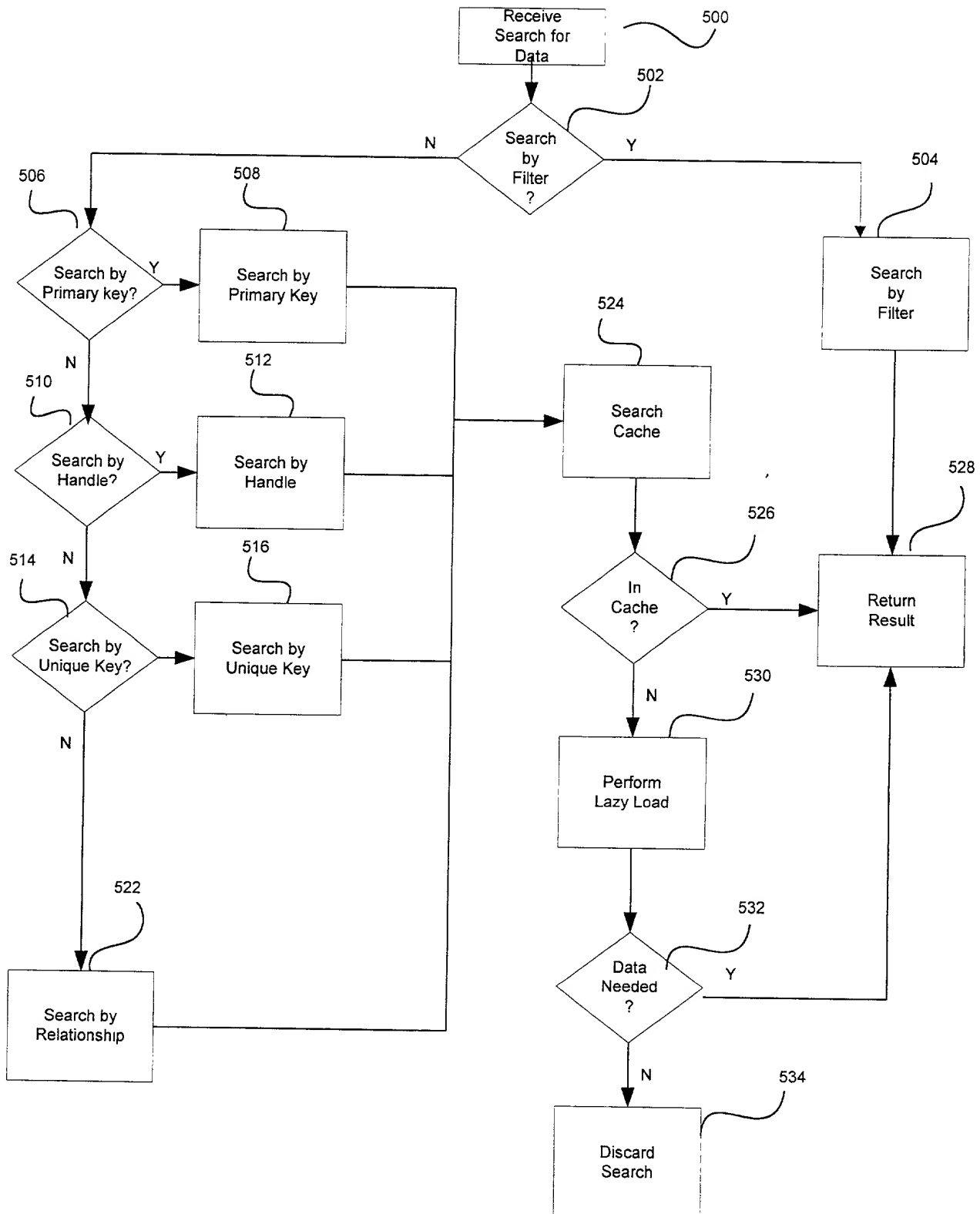


Fig. 5

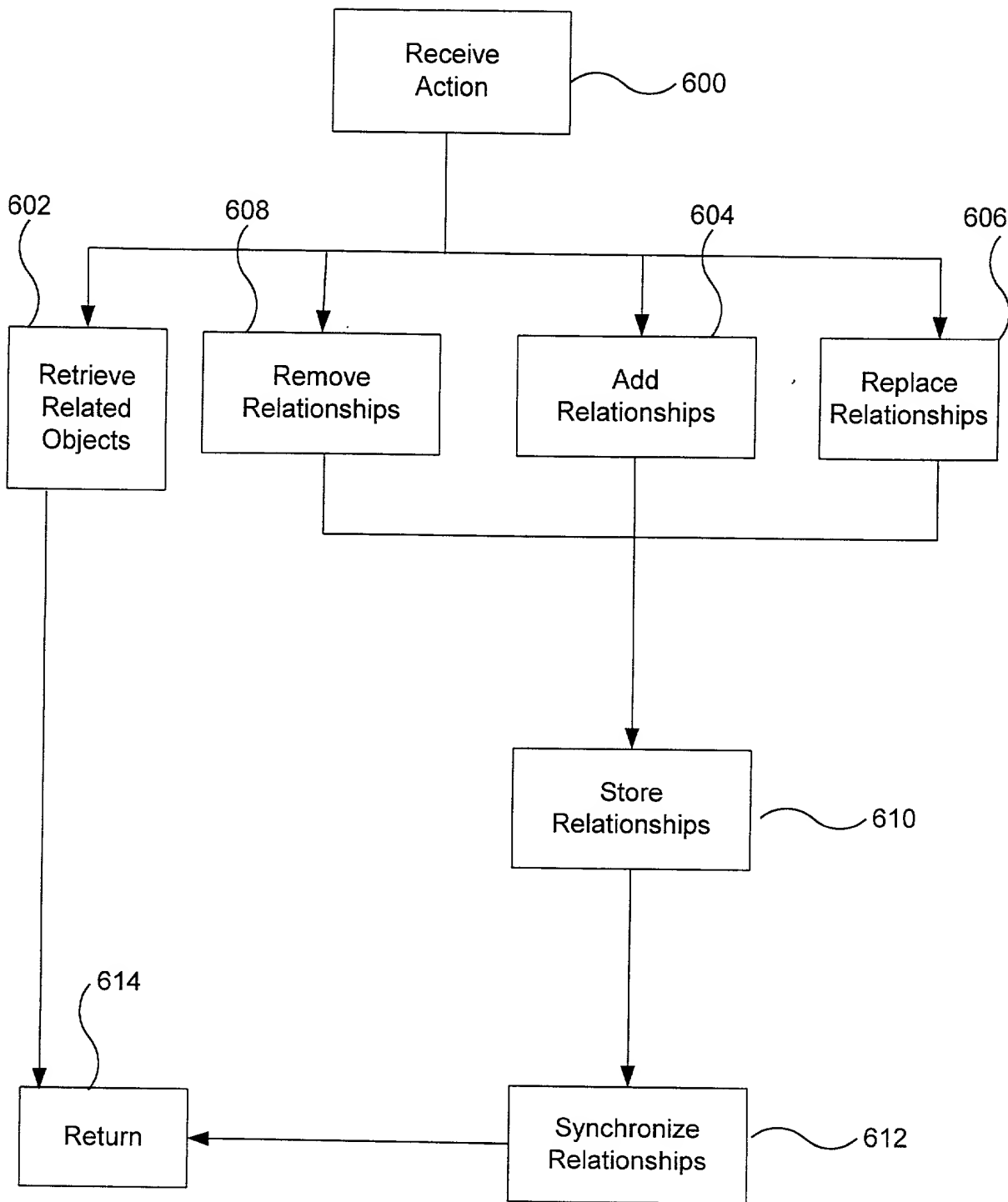


Fig. 6

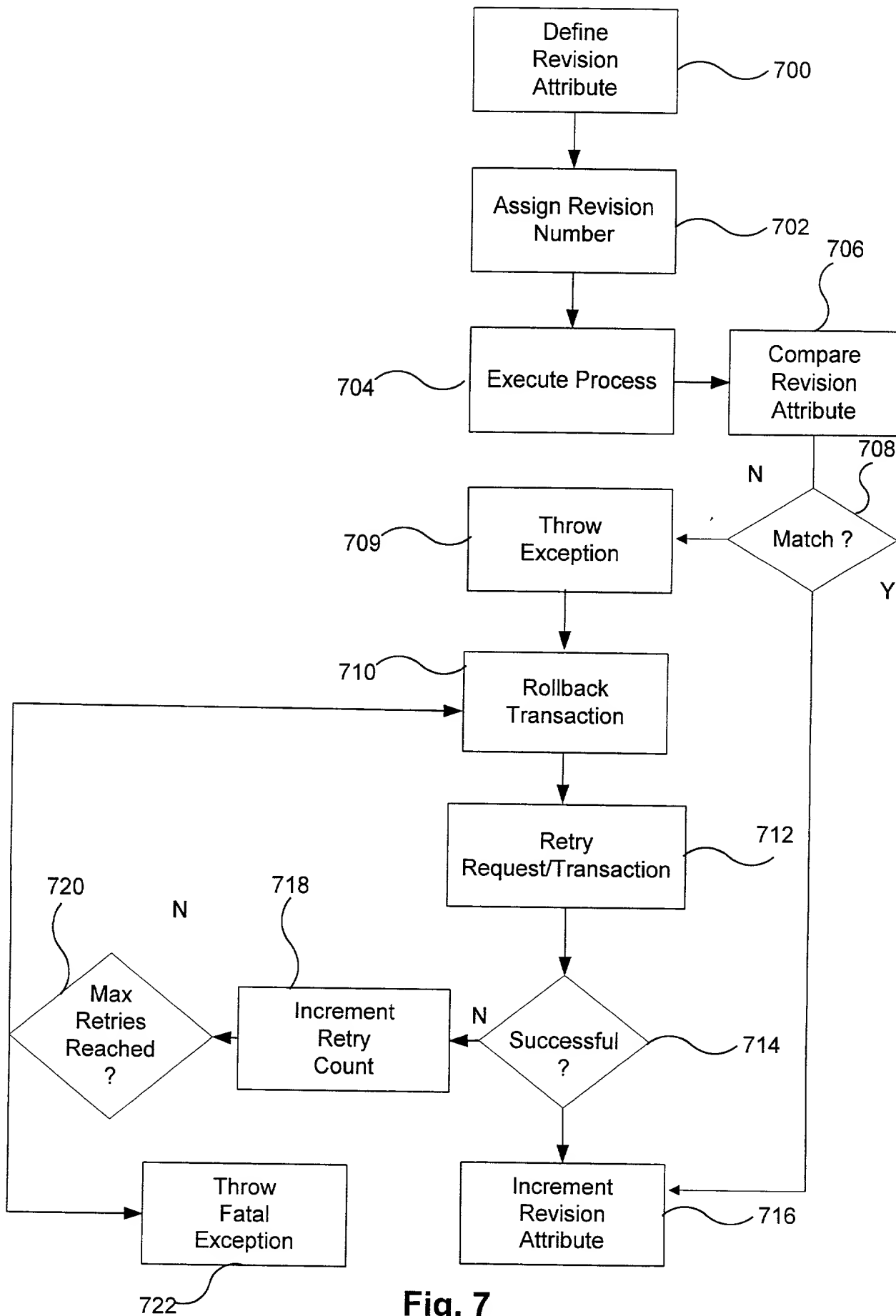


Fig. 7

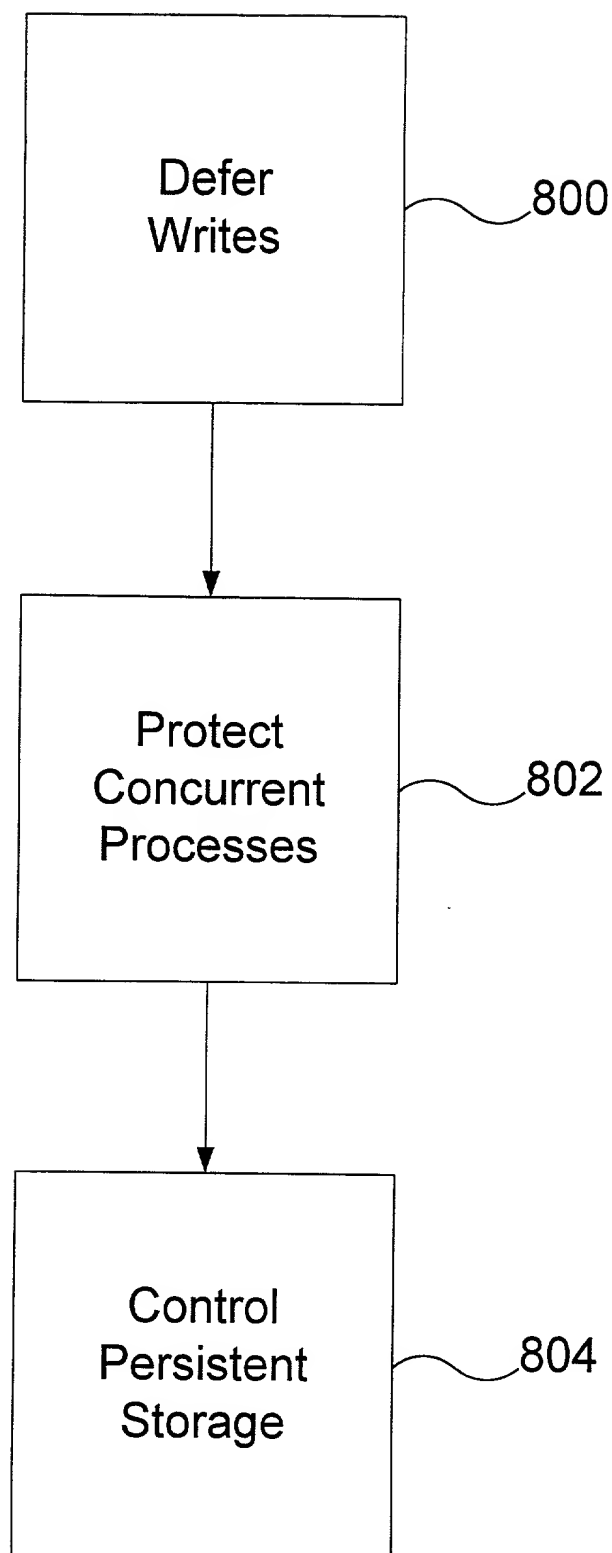


Fig. 8